

ABSTRACT OF THE DISCLOSURE

An image forming apparatus using a synchronization signal generator can easily generate a pixel clock that enables both a magnification correction 5 in a main scanning direction and a correction of expansion and contraction of pixel width in the main scanning direction. Each of pixel clock generation units generates a clock signal by dividing a frequency of a high-frequency clock so as to generate pulses of a 10 reference period, a long period longer than the reference period and a short period shorter than the reference period, and outputs, as the pixel clock, one of the pulses that is designated by an output selection signal. A pixel clock correction data synthesizing unit 15 synthesizes a first selection signal, which is generated base on a time-series distribution of the pulses of each period defined by a first set of data, and a second selection signal, which is based on a time-series distribution of the pulses of each period defined by a 20 second set of data, so as to generate the output selection signal.